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ATTORNEY DOCKET NO.: 08244.0026
CUSTOMER NUMBER: 22,852

Box Patent Application
Assistant Commissioner for Patents
Washington, D.C. 20231

New U.S. Patent Application
Title: METHOD FOR MANUFACTURING INTERLAYER
DIELECTRIC LAYER IN A SEMICONDUCTOR DEVICE

Inventor: Sun-Oo Kim 1) Address: 1) Kyoungki-Do, Korea

Sir:

We enclose the following papers for filing in the United States Patent and Trademark Office in connection with the above patent application.

1. Application - 12 pages, including 1 independent claim and 7 claims total.
2. Drawings - 7 sheets of drawings containing 11 figures.
3. The filing fee is calculated as follows:

Basic Application Filing Fee					\$710	\$ 710.00
	Number of Claims		Basic	Extra Claims		
Total Claims	7	-	20		x \$18	
Independent Claims	1	-	3		x \$80	
[] Presentation of Multiple Dep. Claim(s)					+\$270	
					Subtotal	\$ 710.00
					Reduction by 1/2 if small entity	-
					TOTAL APPLICATION FILING FEE	\$ 710.00

4. A check for \$710.00 is enclosed. The fee includes
\$710.00 filing fee

This application is being filed under the provisions of 37 C.F.R. § 1.53(f).
Applicant awaits notification from the Patent and Trademark Office of the time set for
filing the Declaration.

Applicant claims the right to priority based on Korean Patent Application No.
1999-61786 filed December 24, 1999.

Please address all correspondence with respect to this application to:

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Please accord this application a serial number and filing date.

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Assistant Commissioner for Patents
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Page 3

The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or § 1.17 during the pendency of this application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

By:


Ernest F. Chapman
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EFC/FPD/cb
Enclosures